

FIG. 1

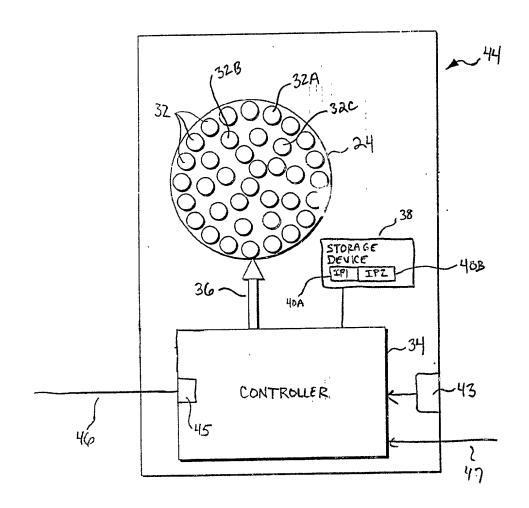


Fig. 2

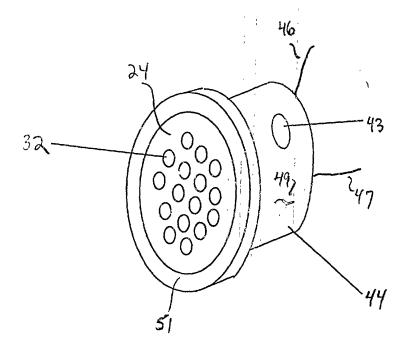
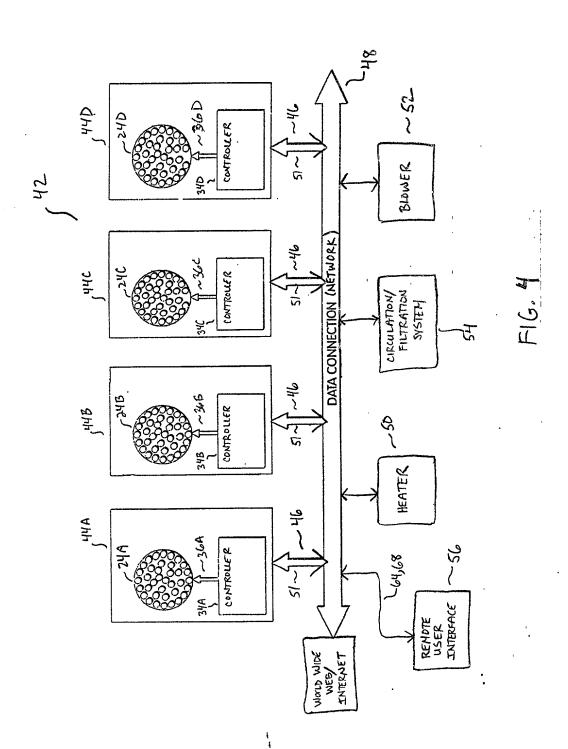


FIG.3



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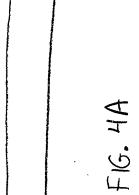
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D	LOGIC LOW	DATA

FIG. 4B

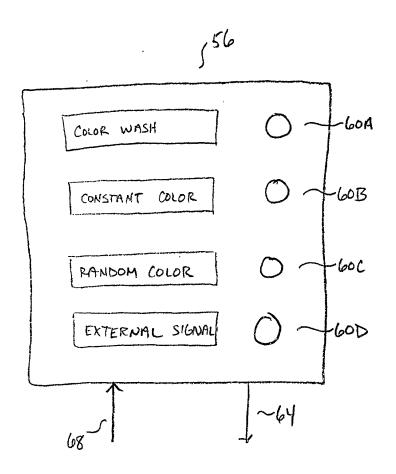


FIG. 5

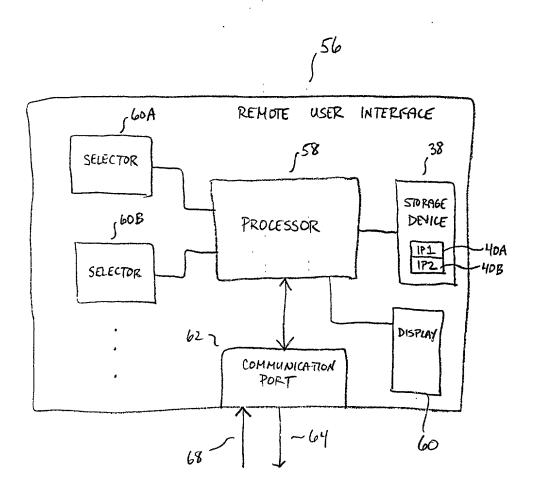


FIG. 6

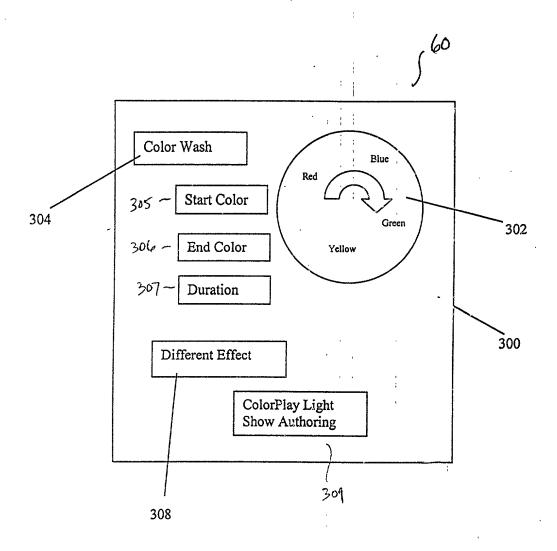


FIG. 7

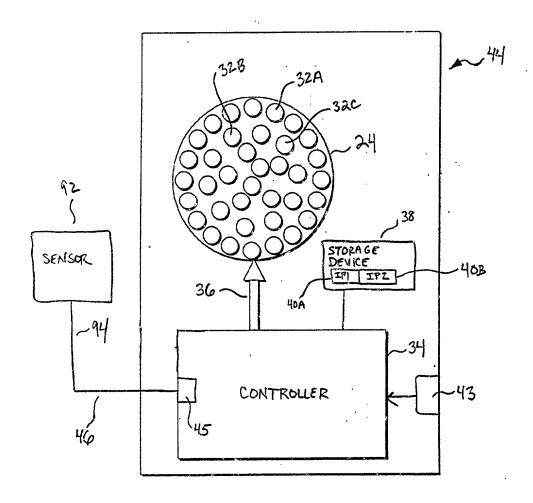
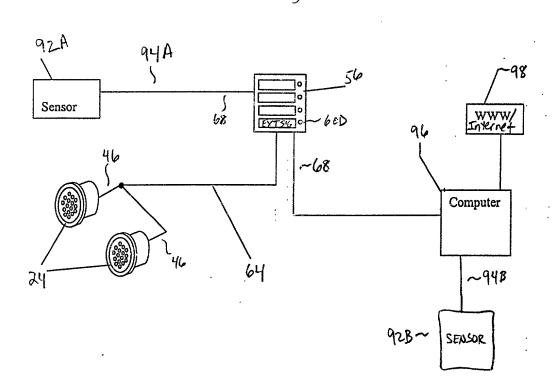
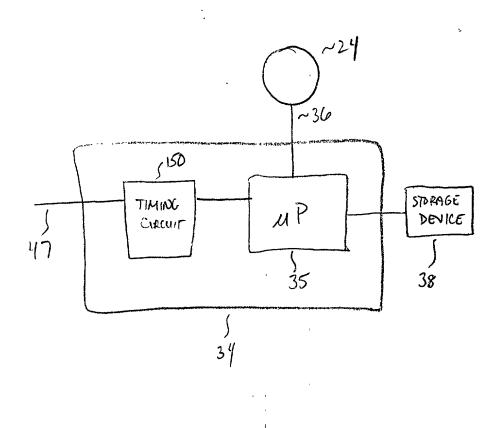


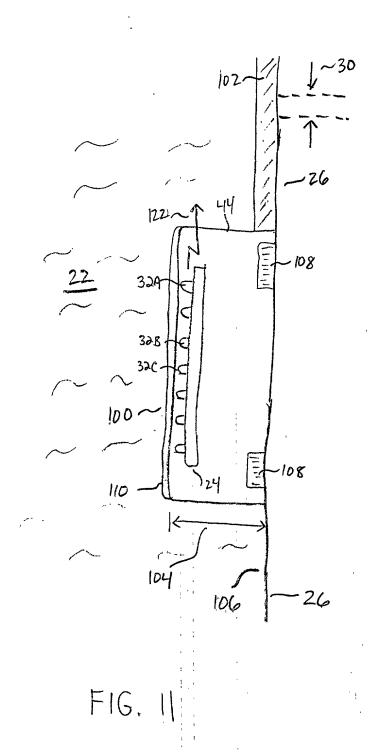
Fig. 8

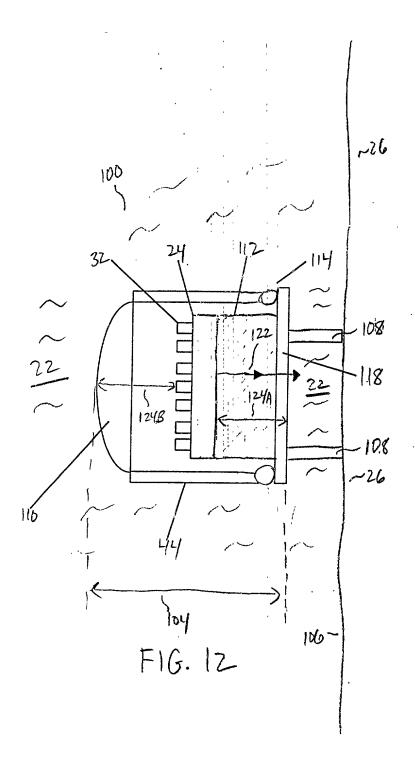


F16.9



F1G. 10





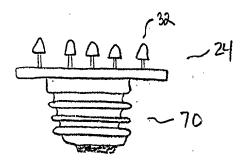


FIG 13

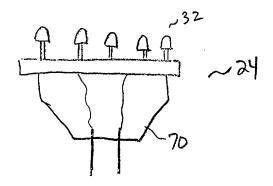


FIG. 14

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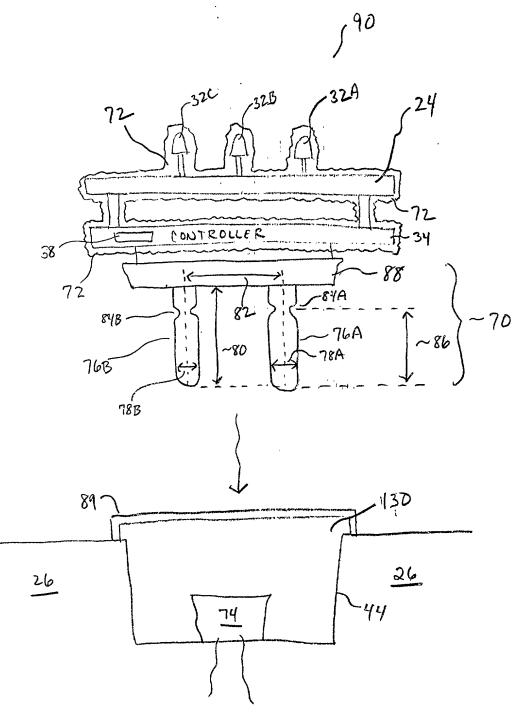
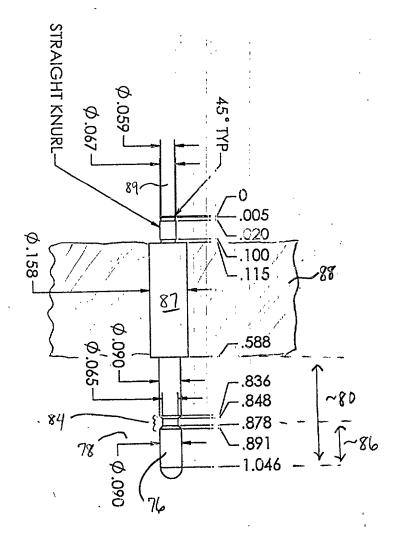


FIG. 15



F16. 16 A



F16. 16B

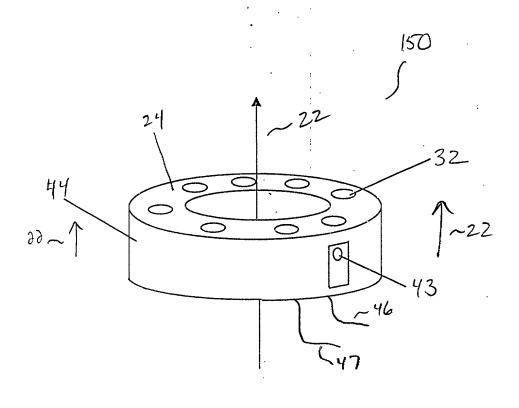
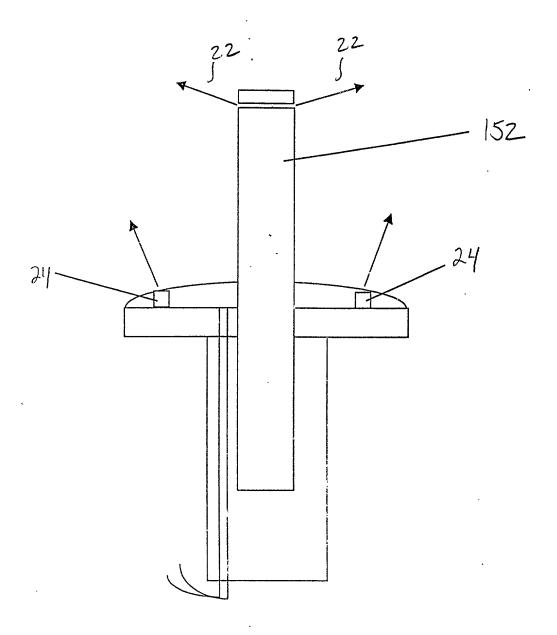
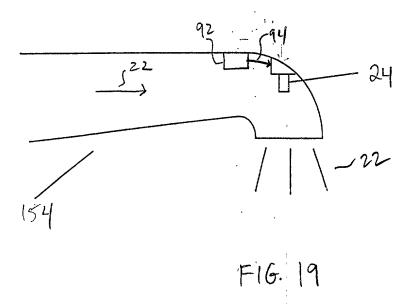
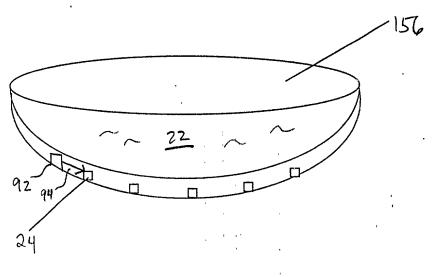


FIG. 17



F16. 18





F16.20